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Date: 7/3/97

By: Patricia K. Parry  
Patricia K. Parry

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of )

Charles H. Moore et al. )

Serial No. 08/484,918 )

Filed: June 7, 1995 )

For: HIGH PERFORMANCE )  
MICROPROCESSOR HAVING )  
VARIABLE SPEED )  
SYSTEM CLOCK )

Examiner: D. Eng

Art Unit: 2315

AMENDMENT

Palo Alto, CA 94306

Assistant Commissioner for Patents  
Washington, D.C. 20231

Sir:

This Amendment is being submitted in response to the Office Action dated April 3, 1997 in the above-identified patent application.

IN THE CLAIMS

Please amend claim 73 as follows:

Sub CB  
73( Twice Amended). A microprocessor system comprising:  
a central processing unit disposed upon an integrated circuit substrate, said central processing unit operating at a processing frequency and being constructed of a first plurality of electronic devices;  
an oscillator disposed upon said integrated circuit substrate and connected to said central processing unit, said oscillator clocking said central processing unit at a clock rate and [including] being constructed of a second plurality of electronic devices, thus varying the [operating characteristics] processing frequency of said first plurality of electronic devices and the clock rate of said second plurality of [transistors] electronic devices in the same way as a function of parameter variation in one or more fabrication or operational parameters associated with said